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EXAMINER

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ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

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Continuation of Substance of Interview including description of the general nature of what was discussed: Courtesy call in interest of compact prosecution, pointing out arguments concerning the term "mirror area". First the term mirror area is not used as one of ordinary skill in the art would commonly accept as it's meaning. The claim language defines the term "mirror area" be an area divided into at least two physical areas each designated to correspond to a same logical area for storing content written to the logical area. This broad claim language and the specification does not recite that the mirror area has anything to do with the "mirroring" or "copying" or "duplication" of data in the particular area, nor does it appear Applicant argues such. However, as previously discussed with appellant in interview, the subject matter as described in FIG. 3, including a fixed association of physical blocks (ZPx) only mapped to a particular logical block (ZL), and the mirror area is either ZPu, ZPu, ZPi, ZPi concatenated (or each ZP is a separate mirror area, ie. the a single ZP is a single mirror area or the four ZP physical blocks are one mirror area), wherein one of the physical blocks in the mirror (ZPu, ZPu, ZPi, ZPi) is the active area containing the actual up-to-date data, while the other physical blocks are inactive containing old data to be erased or already erased. blocks, etc. The claim language does not reflect such interpretations. If properly claimed, and supported by the Specification it appears this subject matter would overcome the prior art of record, and require further search and/or consideration.

/Matt Kim/
SPE, AU2186